

Mitsubishi FX0n/FX2

Mitsubishi FX0s/FX0n/FX1s/FX1n/FX2 PLC

<http://www.mitsubishi-automation.com>

HMI Setting:

Parameters	Recommend	Option	Notes
PLC type	Mitsubishi FX0n/FX2	Mitsubishi FX0n/FX2	
Com port	RS485	RS232/RS485	
Baud rate	9600	9600/19200/38400/57600/ 115200	must same as the PLC setting
Parity bit	Even	Even, Odd, None	must same as the PLC setting
Data Bits	7	7,8	must same as the PLC setting
Stop Bits	1	1,2	must same as the PLC setting
HMI Station No.	0	0-255	Does not apply to this protocol
PLC Station No.	0	0-255	must same as the PLC setting

PLC Setting:

Communication mode	9600,Even,7,1
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Device address:

Bit/Word	Device Type	Format	Range	Memo
B	X	ooo	0-377	Input Relay
B	Y	ooo	0-377	Output Relay
B	M	ddd	0-9999	Auxiliary Relay
B	T	ddd	0-255	Timer Relay
B	C	ddd	0-255	Counter Relay
B	D_Bit	dddd(dd)	0-9999(0~15)	Data Register Bit (D)
B	S	dddd	0-4095	States
B	SM	dddd	8000-9999	Special Aux. Relays
W	TV	ddd	0-255	Timer Memory
W	CV	ddd	0-199	Counter Memory
W	D	ddd	0-9999	Data Register
W	CV2	ddd	200-255	Counter Memory(D Word)
W	SD	ddd	8000-9999	Special Data Register

Wiring diagram:

MT8000

COM1 [RS-485] 4w

9P D-SUB Male

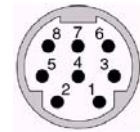
1	RX-
2	RX+
5	GND
3	TX-
4	TX+

Mitsubishi PLC CPU

RS422 Port

8P MiniDin Female

4	TX-
7	TX+
3	GND
1	RX-
2	RX+



8Pin miniDin Female

MT8000

COM1 [RS-485] 4w

9P D-SUB Male

1	RX-
2	RX+
3	TX-
4	TX+
5	GND

Mitsubishi PLC CPU

RS422 Port

25P Female

16	TXD-
3	TXD+
15	RXD-
2	RXD+
7	GND
4	DSR+
8	GND
13	+5V
17	DSR-

Driver Version:

Version	Date	Description
1.10	August 27, 2009	Add S, SM, D_bit address type