

LS XGT/XGK CPU DIRECT

LS XGT/XGK CPU RS232 port

<http://www.lgis.com/>

HMI Setting:

Parameters	Recommend	Option	Notes
PLC type	LS XGT/XGK CPU DIRECT		
Com port	RS232	RS232/RS485	
Baud rate	19200	9600~115200	
Parity bit	None	Even, Odd, None	
Data Bits	8	7, 8	
Stop Bits	1	1	
HMI Station No.	0		
PLC Station no.	0		

PLC Setting:

Communication mode	
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Device address:

Bit/Word	Device Type	Format	Range	Memo
B	P	dddd(h)	0~2047F	I/O device
B	M	dddd(h)	0~2047F	Internal device
B	L	dddd(h)	0~2047F	Communication device
B	K	dddd(h)	0~2047F	Preservation device
B	F	dddd(h)	0~2047F	Special device(write available from 1025)
B	T	dddd	0~2047	Timer device
B	C	dddd	0~2047	Counter device
B	S	ddd(dd)	0~127(99)	Relay for step control
B	D_Bit	dddd(h)	0~19999F	Data register_Bit expression (D0000.0)
B	U_Bit	hhdd(h)	hh:0~3f, dd:0~31 (h):0~f	XGK-CPUE : hh(0~1f)
W	D	dddd	0~19999	Data register
W	U	hh(dd)	0~3f(0~31)	Analog data register XGK-CPUE : hh(0~1f)
W	N	dddd	0~21503	Communication data register
W	Z	ddd	0~127	Index register_128 words
W	T	dddd	0~2047	Timer current value register
W	C	dddd	0~2047	Counter current value register
W	R	dddddd	0~32767	

W	ZR	dddd	0~32767	
W	TS	ddd	0~2047	Setup value
W	CS	ddd	0~2047	Setup value

d:(Decimal) h:(Hexadecimal)

Wiring diagram:

RS-232:

MT8000 RS232
9P D-SUB

XGT main unit
RS232 9P D-SUB

